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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Complete if Known	
		<b>Application Number</b>	09/434,082
		<b>Filing Date</b>	November 5, 1999
		<b>First Named Inventor</b>	Ryan, Kevin
		<b>Group Art Unit</b>	2186
		<b>Examiner Name</b>	Peikari, James
Sheet 1 of 1		Attorney Docket No: 303.306US2	

<b>US PATENT DOCUMENTS</b>				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
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<b>OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BSP		"400 Mb/s/pin SDRAM, 4M x 18 SDRAM, Pipelined, Eight Bank, 2.5V Operation", <u>SLDRAM Inc.</u> , Draft/Advance, SLD4M18DR400, 4 MEG x 18 SDRAM, (1998),pp. 1-69	
BSP		"Draft Standard for A High-Speed Memory Interface (SyncLink)", <u>The Institute of Electrical and Electronics Engineers, Inc.</u> , Draft 0.99 IEEE P1596.7-199X, (1996),pp. 1-55	
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EXAMINER

DATE CONSIDERED

4/28/05